

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

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Filed: 10/6/2003

Examiner:

Serial No.:

Title: CURRENT LIMITING VOLTAGE REGULATION CIRCUIT

Form 1449

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
AB	A	6,285,177	9/4/01	Mallikarjunaswamy et al.	323	315	5/8/2000
AB	B	5,955,915	9/21/99	Edwards	327	541	2/13/96
AB	C	6,426,886	7/30/02	Goder	363	56.1	8/3/01
AB	D	6,069,950	5/30/00	Knollman	379	413	11/6/97
AB	E	5,550,700	8/27/96	Moore, et al.	361	101	10/7/94
AB	F	6,144,187	11/7/00	Bryson	320	137	7/13/99
	G						
	H						
	I						
	J						
	K						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	L							
	M							
	N							
	O							
	P							
	Q							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	R	
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	T	
Examiner	A. Berhane	
	Date Considered	4/05

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.